

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of :  
: Sashikanth Chandrasekaran, et al. : Confirmation Number: 1436  
: :  
Serial No.: 09/871,853/Patent No.: 7,107,319 : Group Art Unit: 2187  
: :  
Filed: May 31, 2001/Issued: September 12, 2006 : Examiner: Kimberly N. McClean-Mayo

For: METHOD AND APPARATUS FOR REDUCING LATENCY AND MESSAGE  
TRAFFIC DURING DATA AND LOCK TRANSFER IN A MULTI-NODE  
SYSTEM

**REQUEST FOR CERTIFICATE OF CORRECTION**

Attention Certificate of Corrections Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Enclosed herein is a proposed Certificate of Correction which Applicants respectfully request be entered with respect to the above patent. A copy of the initialed 1449 by the Examiner makes it clear that the omission of the references printed on the patent is the fault of the PTO. Hence, no fee is required.

It is respectfully requested that the Certificate of Correction be issued.

Respectfully submitted,

HICKMAN PALERMO TRUONG & BECKER LLP

Dated: May 31, 2007

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PAGE 05/06

<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <b>(PTO-1449)</b>				<b>ATTY. DOCKET NO.</b> 50277-1653		<b>SERIAL NO.</b> 09/871,853	
				<b>APPLICANT:</b> Sashikanth Chandrasekaran, et al.			
				<b>FILING DATE:</b> May 31, 2001		<b>GROUP:</b> <del>213</del> 2187	
<b>U.S. PATENT DOCUMENTS</b>							
Exam. Initial*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		Number	Kind Code <sup>2</sup> (If known)				

<b>FOREIGN PATENT DOCUMENTS</b>								
Exam. Initial*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD- YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>3</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (If known)				

<b>OTHER ART - NO PATENT LITERATURE DOCUMENTS</b>			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation <sup>2</sup>
KMM		Lory D. Molesky et al., "Database Locking Protocols for Large-Scale Cache-Coherent Shared Memory Multiprocessors: Design, Implementation and Performance," June 6, 1995, pp. 1-24.	
KMM		Lory D. Molesky et al., "Efficient Locking for Shared Memory Database Systems," March 1994, 28 pages.	

Examiner Signature	<i>[Signature]</i>	Date Considered	July 20, 2006
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard S.3). <sup>4</sup>For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

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## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 7,107,319  
APPLICATION NO. : 09/871,853  
ISSUE DATE : September 12, 2006  
INVENTOR(S) : Sashikanth Chandrasekaran

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:  
(56) References Cited

### OTHER PUBLICATIONS

Please insert

--Lory D. Molesky et al., "Database Locking Protocols for Large-Scale Cache-Coherent Shared Memory Multiprocessors: Design, Implementation and Performance," June 6, 1995, pages 1-24.

Lory D. Molesky et al., "Efficient Locking for Shared Memory Database Systems," March 1994, 28 pages.--

### MAILING ADDRESS OF SENDER (Please do not use customer number below):

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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